International Rectifier

40CPQ050PbF 40CPQ060PbF

SCHOTTKY RECTIFIER

40 Amp

$$I_{F(AV)} = 40Amp$$

 $V_R = 50 -60V$

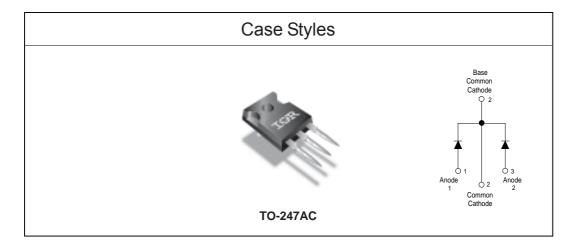
Major Ratings and Characteristics

Characteristics		Values	Units
1 (AV)	ectangular aveform	40	А
V _{RRM}		50/60	V
I _{FSM} @	tp=5µssine	3200	А
	20 Apk, T _J =125°C er leg)	0.49	V
T _J		-55 to 150	°C

Description/Features

The 40CPQ...PbF center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150° C junction temperature. Typical applications are in switching power supplies, converters, free-wheeling diodes, and reverse battery protection.

- \bullet 150° C T $_{\rm J}$ operation
- Center tap TO-247 package
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead-Free ("PbF" suffix)



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40CPQ050PbF, 40CPQ060PbF

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Voltage Ratings

Part number	40CPQ050	40CPQ060	
V _R Max. DC Reverse Voltage (V)			
V _{RWM} Max. Working Peak Reverse Voltage (V)	50	60	

Absolute Maximum Ratings

	Parameters	40CPQ	Units	Conditions	
I _{F(AV)} Max. Average Forward Current *See Fig. 5		40	Α	50% duty cycle @ T _C = 120 °C, rectangular wave fo	
I _{FSM}	Max. Peak One Cycle Non-Repetitive	3200	Α	5μs Sine or 3μs Rect. pulse	Following any rated load condition and with
	Surge Current (Per Leg) * See Fig. 7	320		10ms Sine or 6ms Rect. pulse	rated V _{RRM} applied
E _{AS}	E _{AS} Non-Repetitive Avalanche Energy (Per Leg)		mJ	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 2 \text{Amps}, L = 9.0 \text{mH}$	
I _{AR}	I _{AR} Repetitive Avalanche Current (Per Leg)		Α	Current decaying linearly to zero Frequency limited by T _J max.	

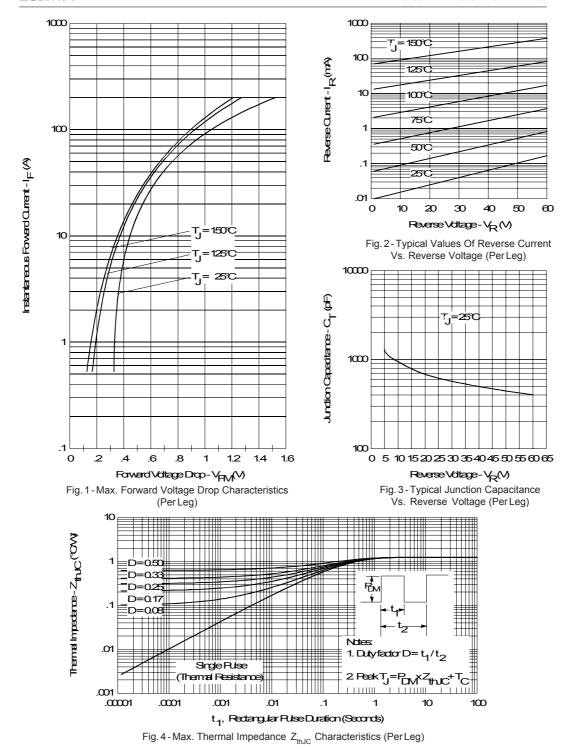
Electrical Specifications

	Parameters		Units	Conditions	
V_{FM}	Max. Forward Voltage Drop	0.53	V	@ 20A	T,= 25 °C
'''	(Per Leg) * See Fig. 1 (1)	0.68	V	@ 40A	1 _J = 23 G
		0.49	V	@ 20A	T 405 °C
		0.64	V	@ 40A	T _J = 125 °C
I _{RM}	Max. Reverse Leakage Current	1.7	mA	T _J = 25 °C	V _P = rated V _P
	(Per Leg) * See Fig. 2 (1)	96	mA	T _J = 125 °C	V _R = rated V _R
C _T	Max. Junction Capacitance (Per Leg)	1600	pF	$V_R = 5V_{DC}$ (test signal range 100Khz to 1Mhz) 25°C	
L _s	Typical Series Inductance (Per Leg) 7.5		nΗ	Measured lead to lead 5mm from package body	
dv/dt	dv/dt Max. Voltage Rate of Change		V/ µs	(Rated V _R)	

(1) Pulse Width < 300µs, Duty Cycle <2%

Thermal-Mechanical Specifications

	Parameters		40CPQ	Units	Conditions
T _J	Max. Junction Temperature Range		-55 to 150	°C	
T _{stg}	Max. Storage Temperature Range		-55 to 150	°C	
R _{thJC}	Max. Thermal Resistance Junction to Case (Per Leg)		1.25	°C/W	DC operation *See Fig. 4
R _{thJC}	Max. Thermal Resistance Junction to Case (Per Package)		0.63	°C/W	DC operation
R _{thCS}	Typical Thermal Resistance, Case to Heatsink		0.24	°C/W	Mounting surface, smooth and greased
wt	Approximate Weight		6 (0.21)	g (oz.)	
Т	Mounting Torque	Min.	6 (5)	Kg-cm	Non-lubricated threads
		Max.	12 (10)	(lbf-in)	
	Case Style TO-247AC		TO-3P)	JEDEC	
	Marking Device		40CPQ050		
			40CPC	060	



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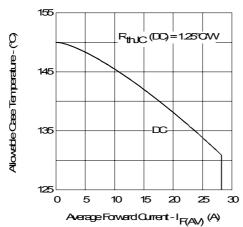


Fig. 5 - Max. Allowable Case Temperature Vs. Average Forward Current (Per Leg)

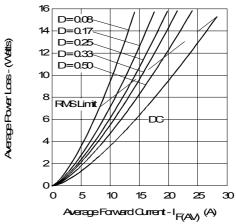


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

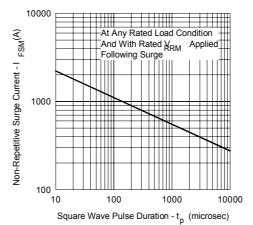


Fig. 7 - Max. Non-Repetitive Surge Current (Per Leg)

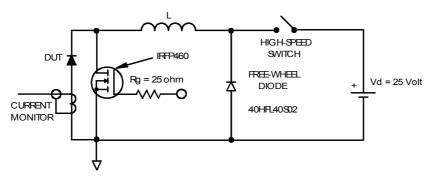
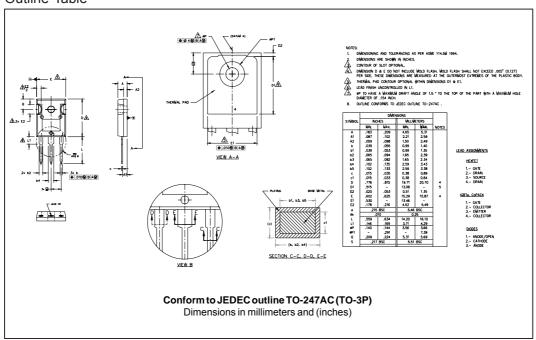
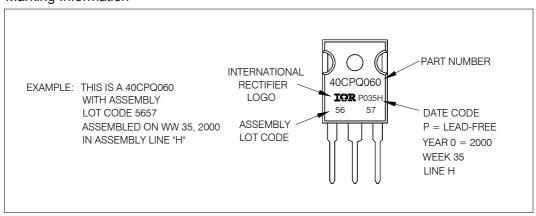


Fig. 8 - Unclamped Inductive Test Circuit

Outline Table

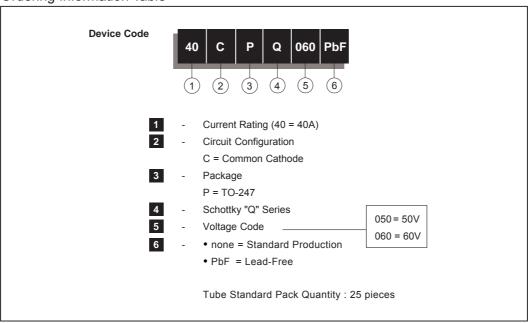


Marking Information



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Ordering Information Table



Data and specifications subject to change without notice. This product has been designed and qualified for Industrial Level and Lead-Free. Qualification Standards can be found on IR's Web site.



IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105
TAC Fax: (310) 252-7309
11/06



Vishay

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